

A green rectangular road sign with rounded corners is mounted on a wooden post. The sign contains the text "The eGaN® FET Journey Continues" in white. The background of the entire image is a landscape with a road leading towards a building under a cloudy sky.

The eGaN® FET
Journey Continues

**eGaN® FET based Wireless Energy Transfer
Using New Zero Voltage Switching Class-D Topology**

Efficient Power Conversion Corporation

- Wireless Energy Transfer Overview
- Topology Overview
- eGaN[®]FET Family
- Wireless FET Figure of Merit
- Experimental performance
- Preliminary A4WP Results
- Summary

eGaN[®] is a registered trademark of Efficient Power Conversion Corporation

- Mobile device charging
 - Convenience
 - Extended battery life
- Medical Implants
 - Quality of life improvement
 - Life extender
- Hazardous environment systems
 - Explosive atmosphere
 - Corrosive locations
 - High Voltage



- Alliance for Wireless Power (A4WP / Rezence)

- Highly resonant (6.78 MHz ISM band)
- loosely coupled coils



- Wireless Power Consortium (WPC - Qi)

- Low frequency (~ 100 - 205 kHz)
- Tightly coupled (Inductive)



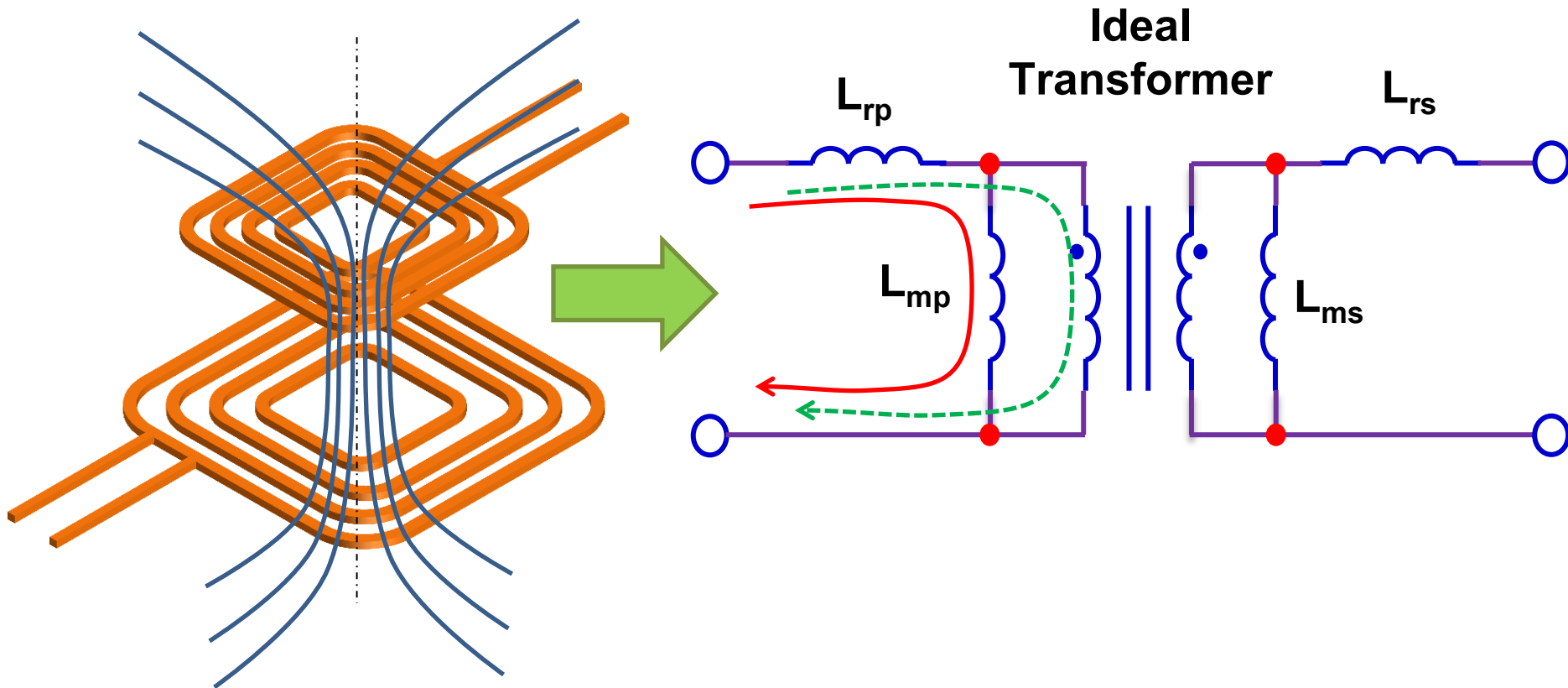
- Power Matters Alliance (PMA)

- Low frequency (~ 201 - 315 kHz)
- Tightly coupled (Inductive)
- Joined with A4WP standard

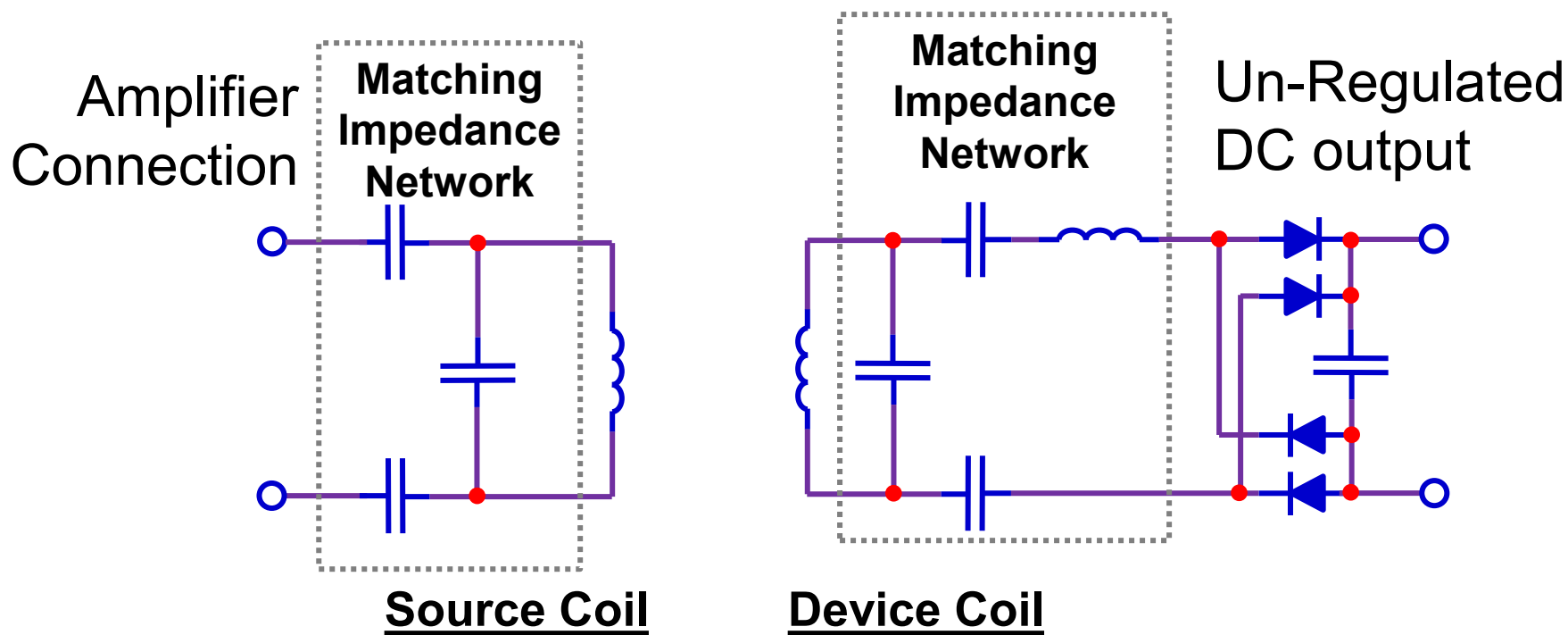


Comprises 4 main sections:

1. An amplifier (a.k.a. a power converter).
2. A Source coil (transmitter) with matching.
3. A Device coil (receiver) with matching.
4. A rectifier with high frequency filtering

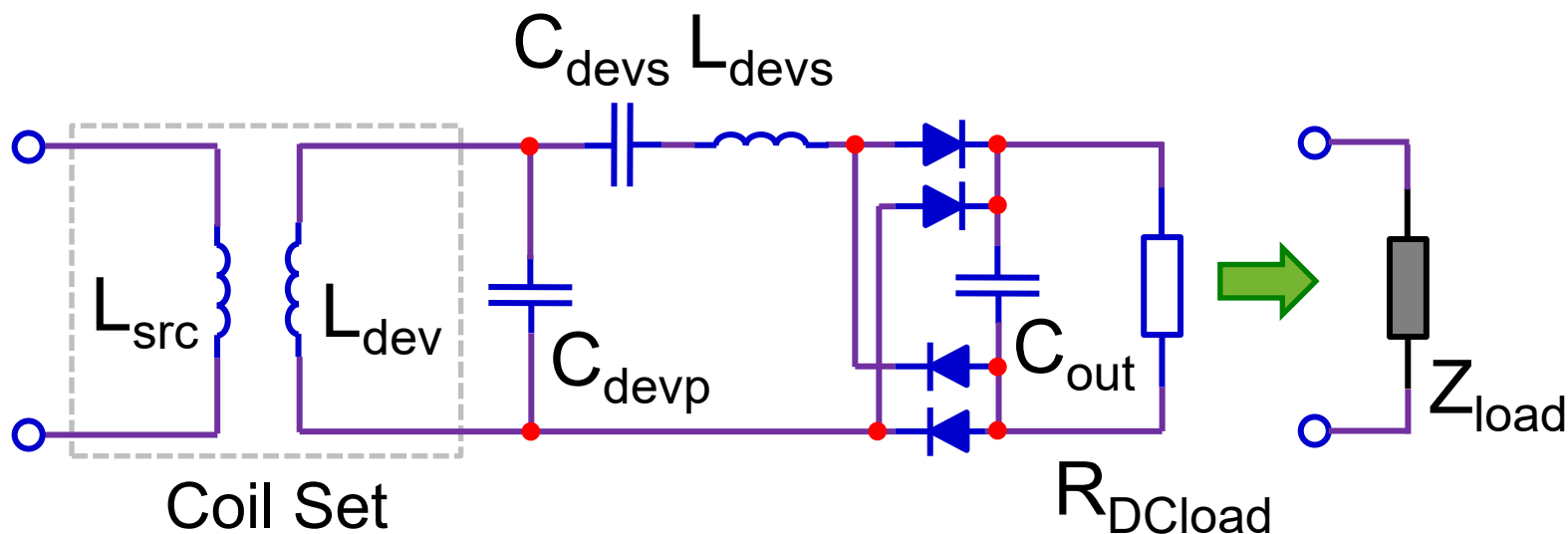


- Coils tuned to resonate at 6.78 MHz
- Series and Shunt tuning can be used
- Coupling and load variation can shift resonance



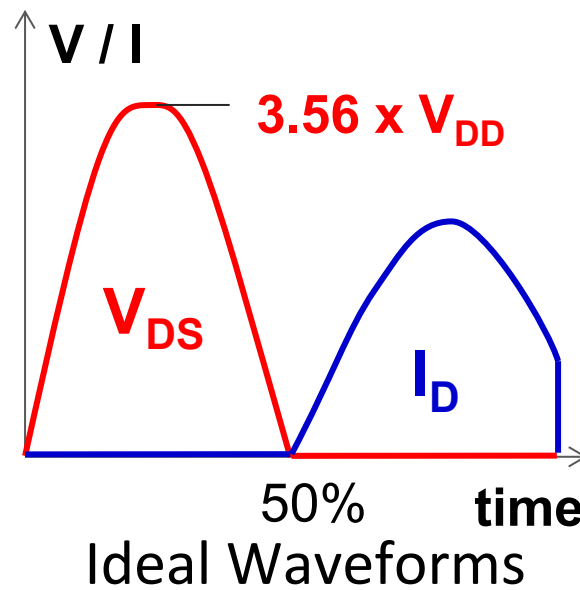
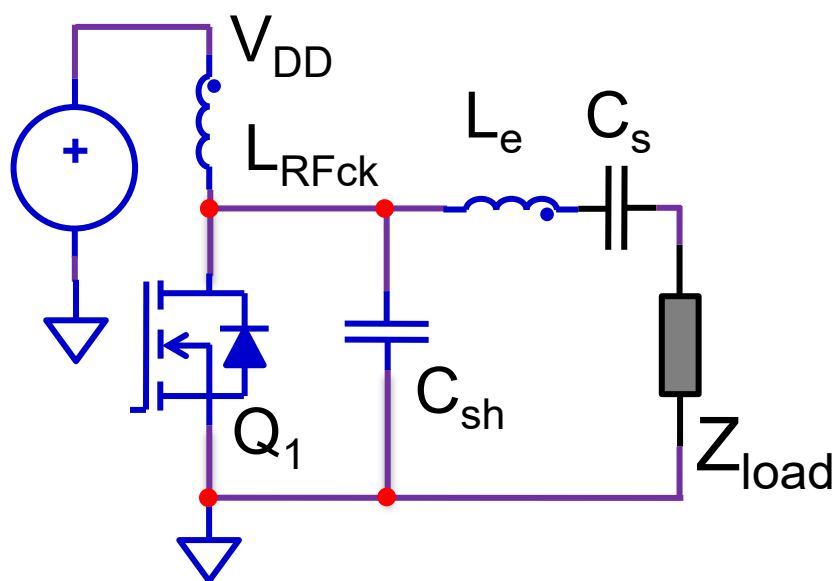
- High Efficiency required
 - limited thermal dissipation budget
 - Energy Standards
- Low Profile – mobile market
- Robustness to dynamic operating conditions (convenience factor)
 - Load Profile – A4WP (inc. Foreign object)
- Regulatory compliance (e.g. FCC, EN, UL)

Simplified representation of coil-set for easy comparison between topologies

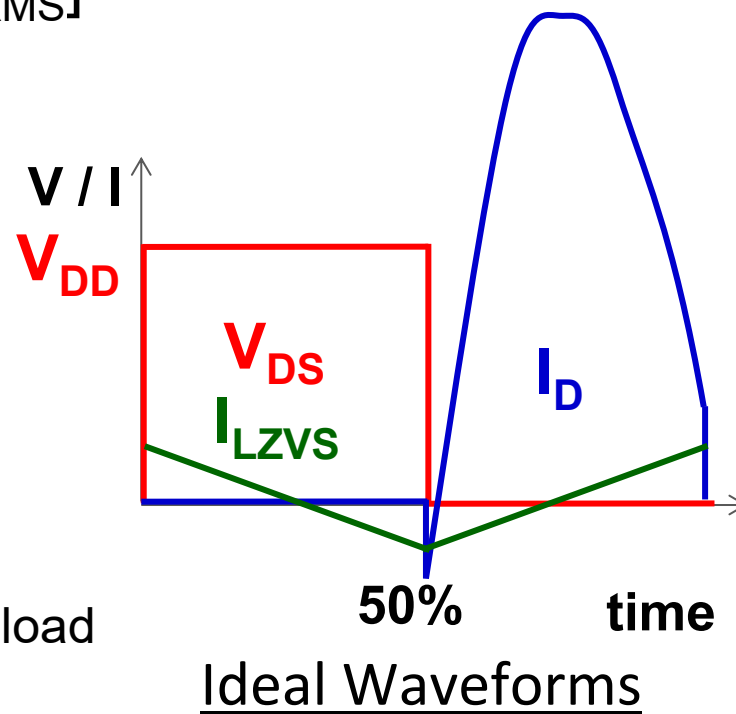
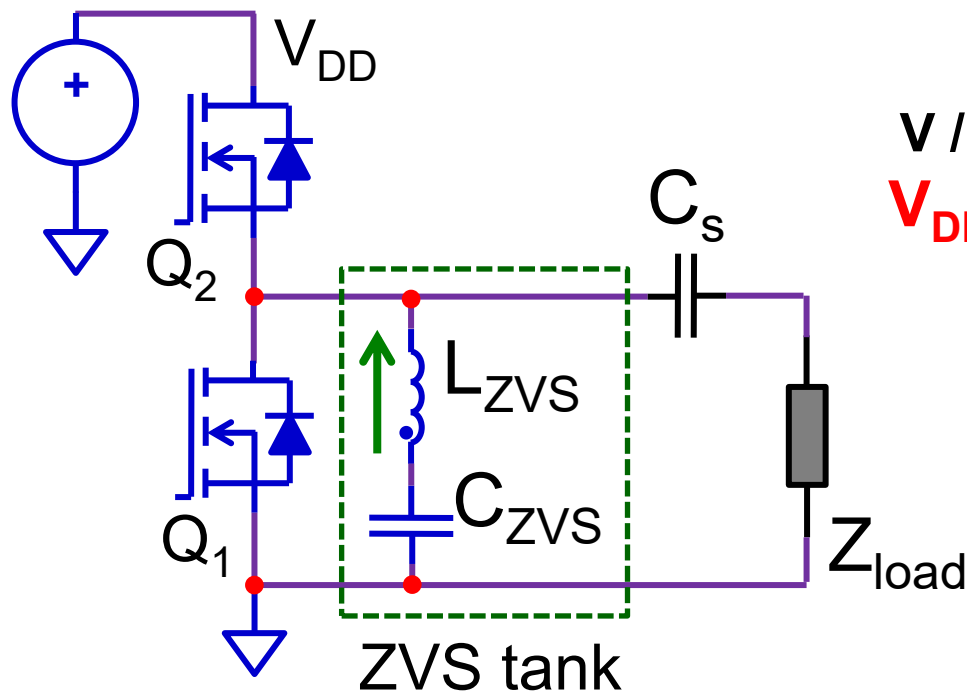


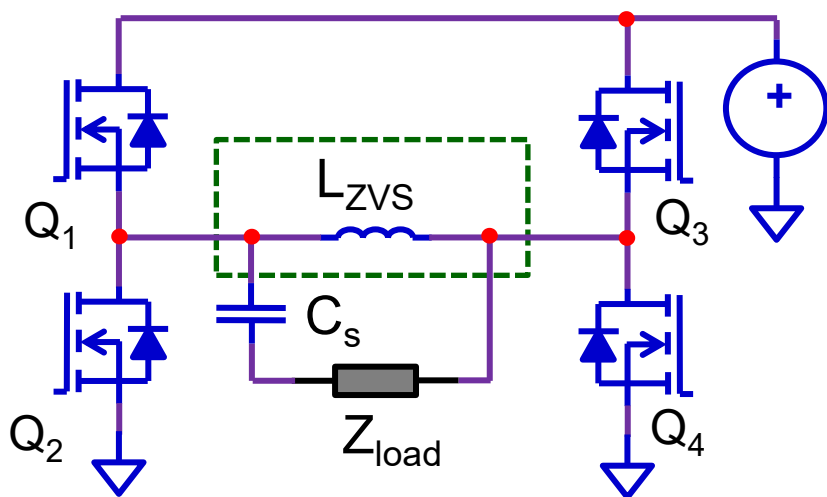
Class-E Overview

- Switch voltage rating $\geq 3.56 \cdot \text{Supply } (V_{DD})$.
- C_{OSS} “absorbed” into matching network.
- Susceptible to load variation - high FET losses
- Tuned Coil Voltage $\approx 0.707 \cdot V_{DD} [V_{RMS}]$

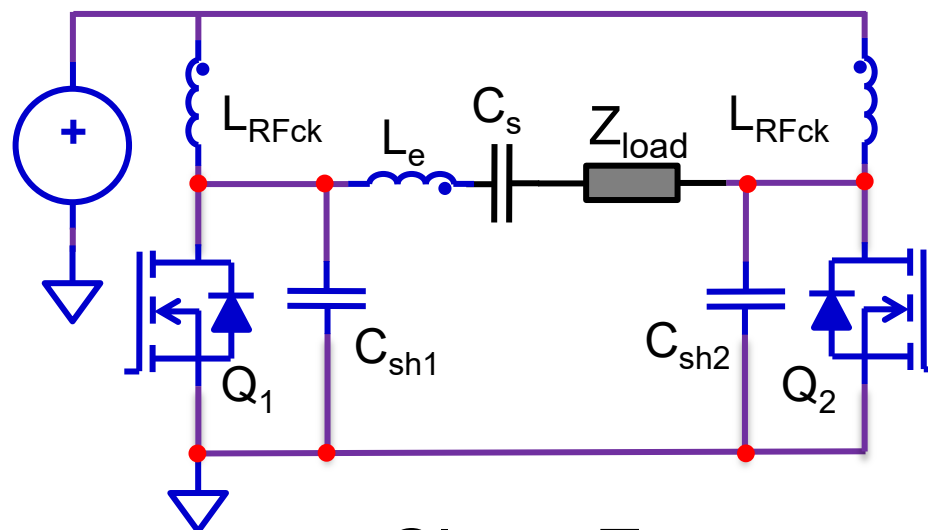


- Switch voltage rating = Supply (V_{DD}).
- ZVS tank current soft switches C_{OSS} Voltage
- ZVS tank circuit does not carry load current
- Tuned Coil Voltage = $\frac{1}{2} \cdot V_{DD} [V_{RMS}]$



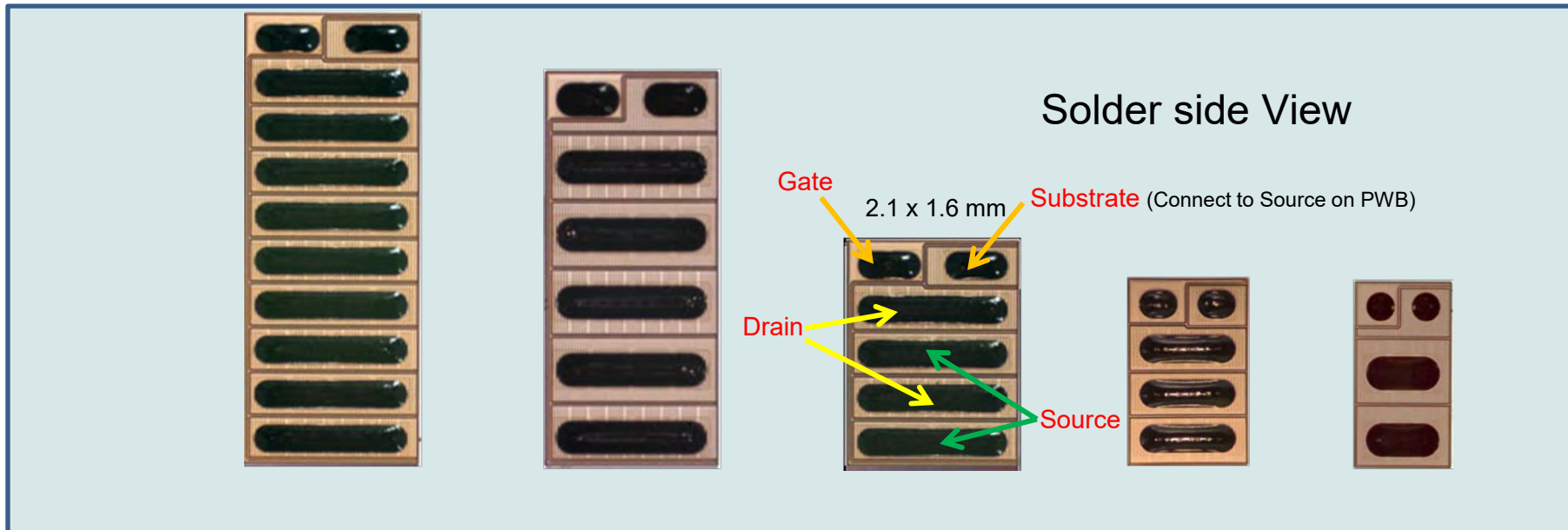


ZVS Class-D



Class-E

- ZVS Class-D simplifies with the removal of the ZVS Capacitor



Part Number	Package (mm)	V_{DS} (V)	V_{GS} (V)	$R_{DS(on)}$ @5V (m Ω)	Q_G @5 V Typ. (nC)	Q_{GS} Typ. (nC)	Q_{GD} Typ. (nC)	R_G Typ. (Ω)	V_{th} Typ. (V)	Q_{RR} (nC)	I_D (A)	T_J Max. ($^{\circ}C$)
EPC2015	LGA 4.1x1.6	40	6	4	10.5	3	2.2	0.6	1.4	0	33	150
EPC2014	LGA 1.7x1.1	40	6	16	2.5	0.67	0.48	0.6	1.4	0	10	150
EPC2001	LGA 4.1x1.6	100	6	7	8	2.3	2.2	0.6	1.4	0	25	125
EPC2016	LGA 2.1x1.6	100	6	16	4.1	0.93	0.75	0.6	1.4	0	11	125
EPC2007	LGA 1.7x1.1	100	6	30	2.1	0.5	0.6	0.6	1.4	0	6	125
EPC2010	LGA 3.6x1.6	200	6	25	5	1.3	1.7	0.6	1.4	0	12	125
EPC2012	LGA 1.7x0.9	200	6	100	1.5	0.33	0.57	0.6	1.4	0	3	125

Ultra High Frequency Gen 3 eGaN FETs



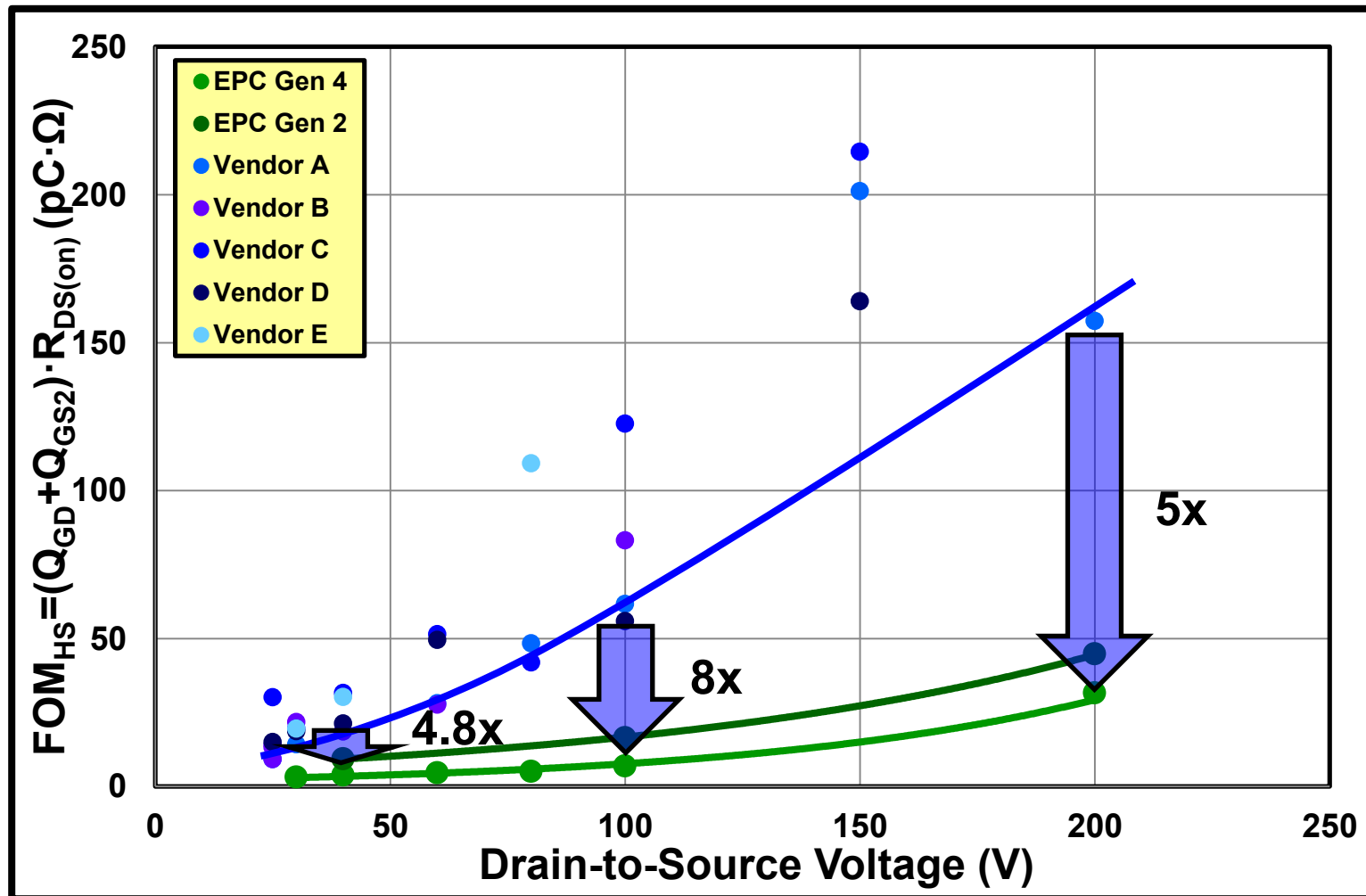
EPC Part No.	BV (V)	Max. $R_{DS(ON)}$ (m Ω) ($V_{GS} = 5V$, $I_D = 0.5 A$)	Min. Peak Id (A) (Pulsed, 25 $^{\circ}C$, $T_{pulse} = 300$ μs)	Typical Charge (pC)					Typical Capacitance (pF) ($V_{DS} = 20 V$; $V_{GS} = 0 V$)		
				Q_G	Q_{GD}	Q_{GS}	Q_{OSS}	Q_{RR}	C_{ISS}	C_{OSS}	C_{RSS}
EPC8004	40	125	7.5	358	31	110	493	0	45	17	0.4
EPC8007	40	160	6	302	25	97	406	0	39	14	0.3
EPC8008	40	325	2.9	177	12	67	211	0	25	8	0.2
EPC8009	65	138	7.5	380	36	116	769	0	47	17	0.4
EPC8005	65	275	3.8	218	18	77	414	0	29	9.7	0.2
EPC8002	65	530	2	141	9.4	59	244	0	21	5.9	0.1
EPC8003	100	300	5	315	34	110	1100	0	38	18	0.2
EPC8010	100	160	7.5	354	32	109	1509	0	47	18	0.2

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Gen 4 Datasheet Summary

Part Number	Gen	BV (V)	R _{DS(on)} (mΩ) (V _{GS} = 5V, at I _D Cont.)		Peak I _D (A) (Pulsed 25°C)	Max T _J	Typical Charge (nC) @ V _{ds} = BV/2;					Typ R _g (Ω)	Cont. I _D (A)
			Typ.	Max			Q _G	Q _{GD}	Q _{GS}	Q _{OSS}	Q _{RR}		
EPC2023	4	30	1.0	1.3	590 A	150°C	27.5	1.9	5.8	27	0	0.3	60
EPC2024	4	40	1.2	1.5	550 A	150°C	26	2.0	6.4	32	0	0.3	60
EPC2020	4	60	1.5	2.0	470 A	150°C	22	2.0	5.0	42	0	0.3	60
EPC2021	4	80	1.8	2.5	420 A	150°C	20	2.1	3.8	60	0	0.3	60
EPC2022	4	100	2.4	3.2	360 A	150°C	17	2.0	3.7	60	0	0.3	60
EPC2019	4	200	33	43	42 A	125°C	2	0.33	0.63	17.5	0	0.3	9
EPC2015	2	40	3.2	4	150 A	125°C	10.5	2.2	3	18.5	0	0.3	33
EPC2001	2	100	5.6	7	100 A	125°C	8	2.2	2.3	35	0	0.3	25
EPC2012	2	200	70	100	15 A	125°C	1.5	0.57	0.33	11	0	0.3	3

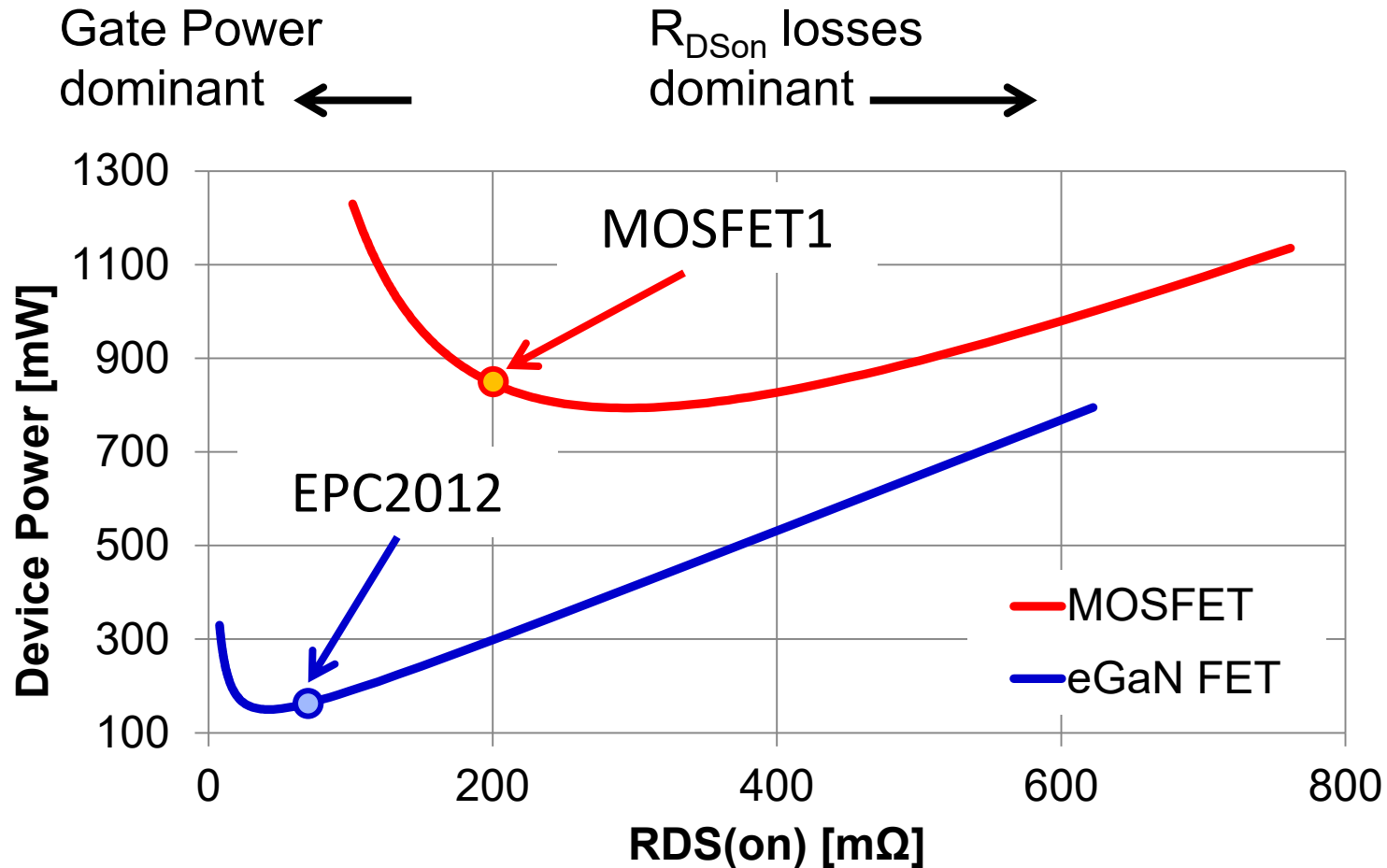
Hard Switching FOM



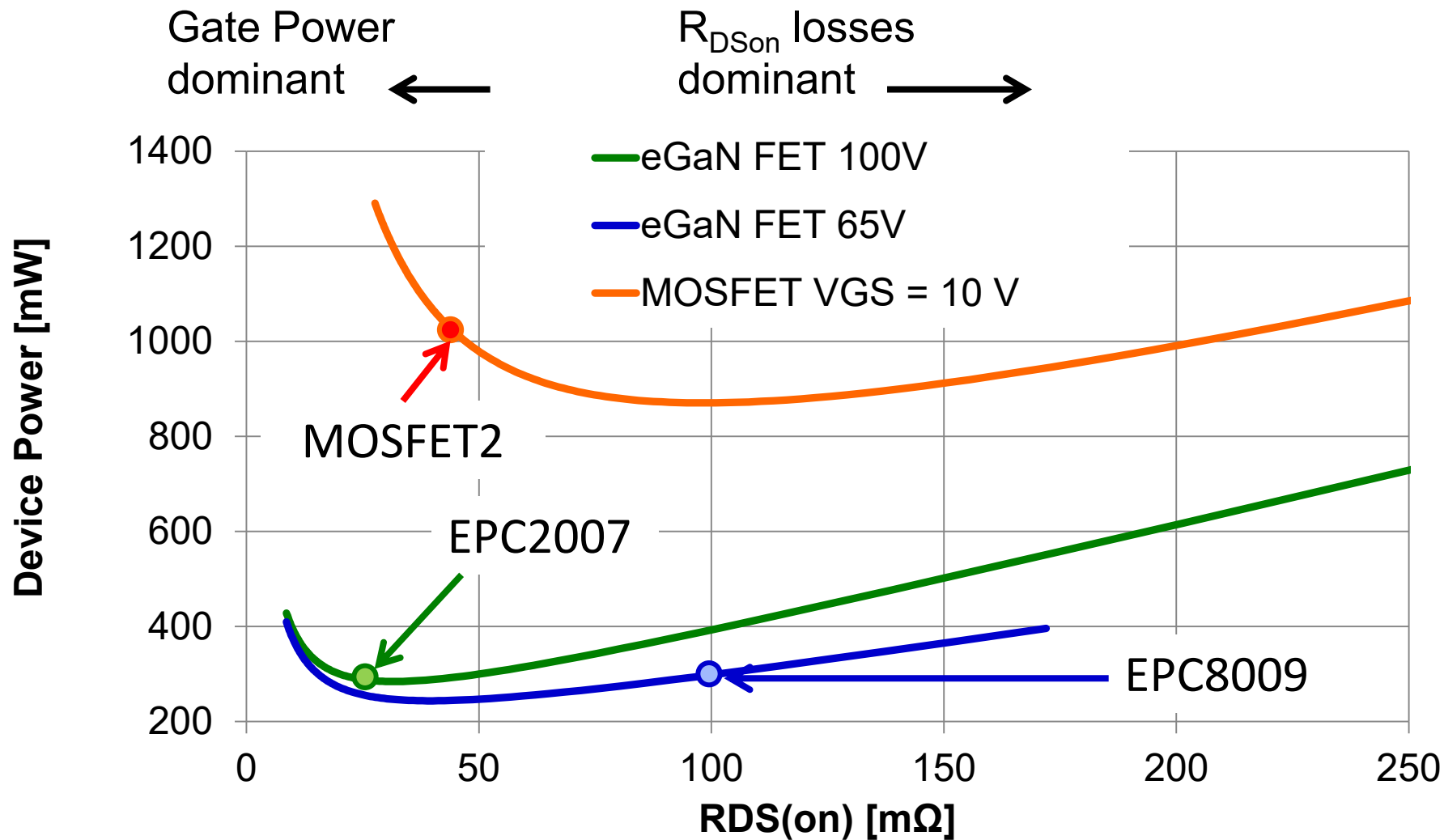
$$V_{DS} = 0.5 \cdot V_{DSS}, I_{DS} = 20 \text{ A}$$

- All topologies are ZVS: $Q_G - Q_{GD}$ only
- C_{OSS} is “absorbed” in matching – excluded
- C_{OSS} still important:
 - Drives off-resonance losses
 - Determines design-ability (Maximum C_{OSS})

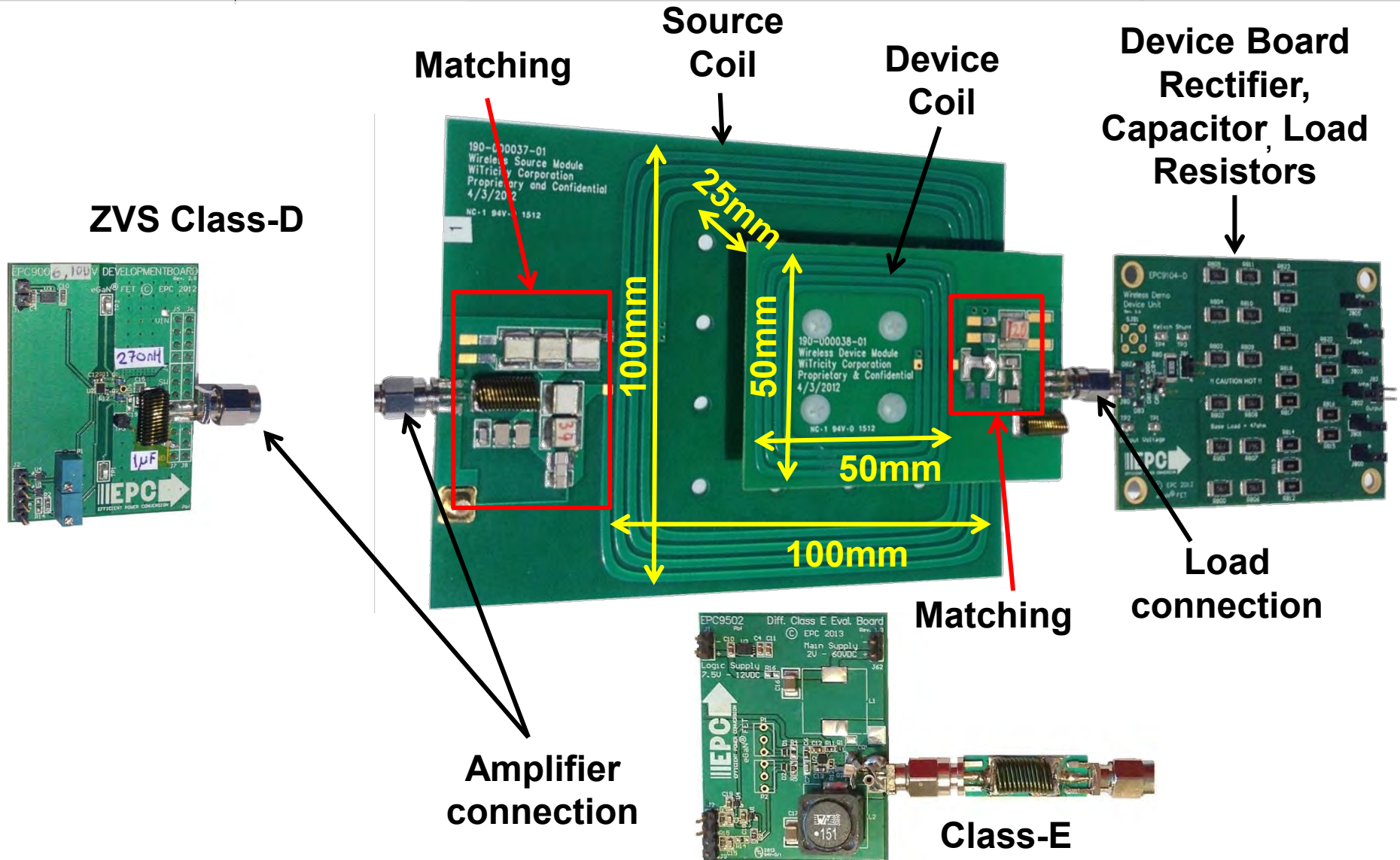
$$FOM_{WPT} = R_{DS(on)} \cdot (Q_G - Q_{GD})$$



A too low $R_{DS(on)}$ yields too high C_{OSS} to realize design!

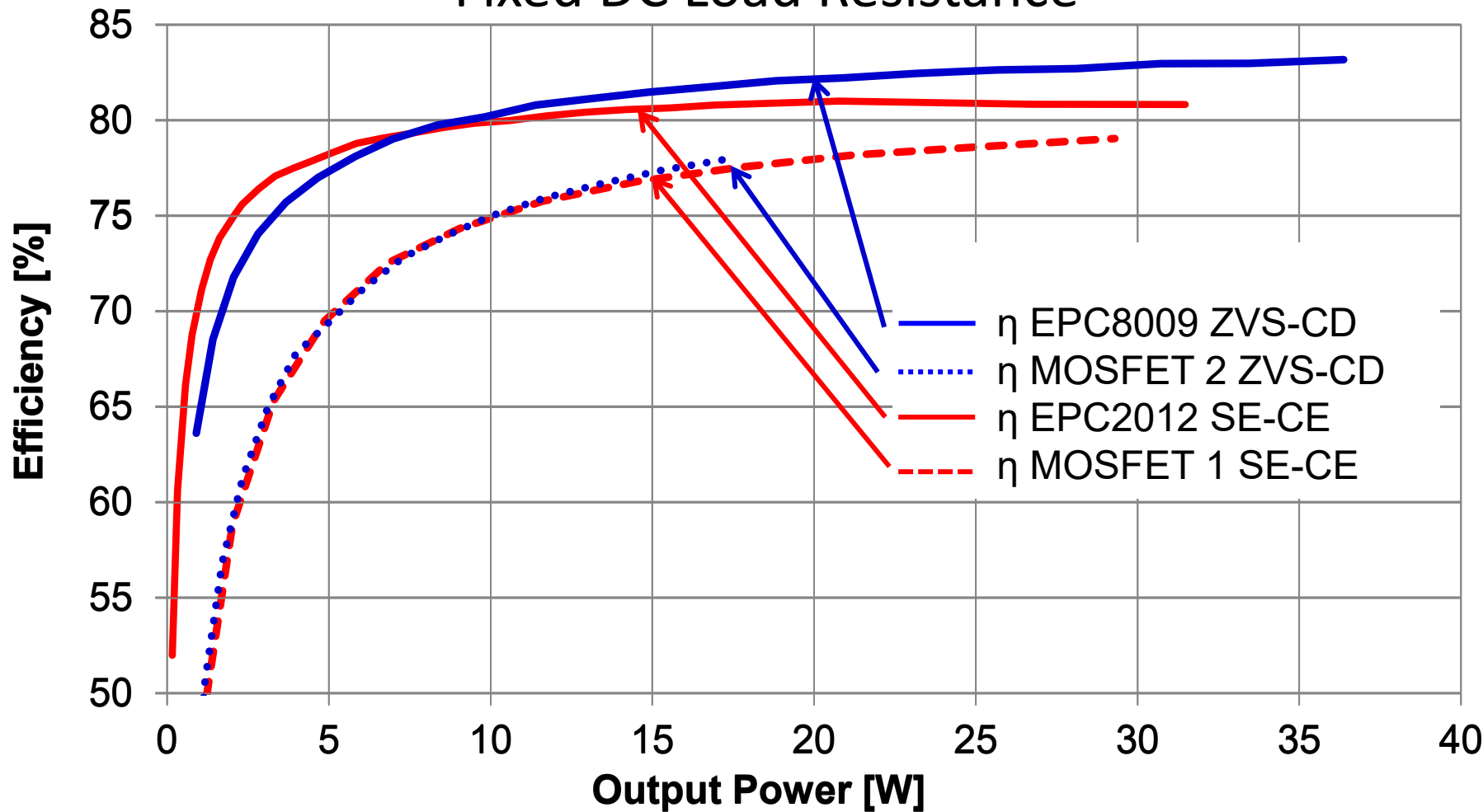


Experimental Setup

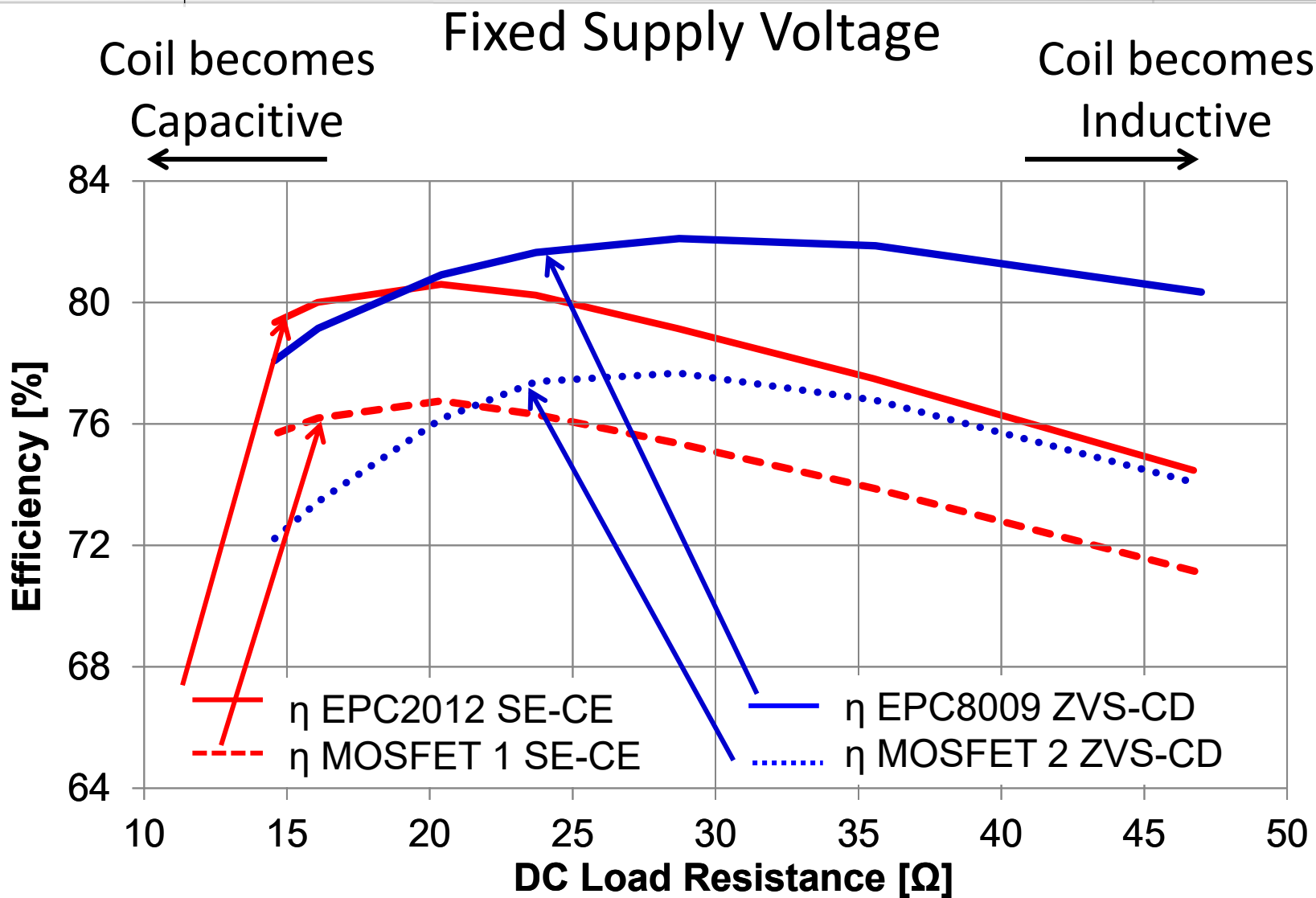


- Operating setup:
 - On resonance tuned source coil
 - Device tuning is fixed
- Performance testing:
 - Fixed load, variable supply (Peak Performance)
 - Fixed supply, variable DC load (3:1 ratio) (Load Variation)
 - Fixed load voltage, variable DC load (3:1 ratio) (Load Regulation)
 - Fixed supply voltage, foreign object response

Variable Supply Voltage Fixed DC Load Resistance

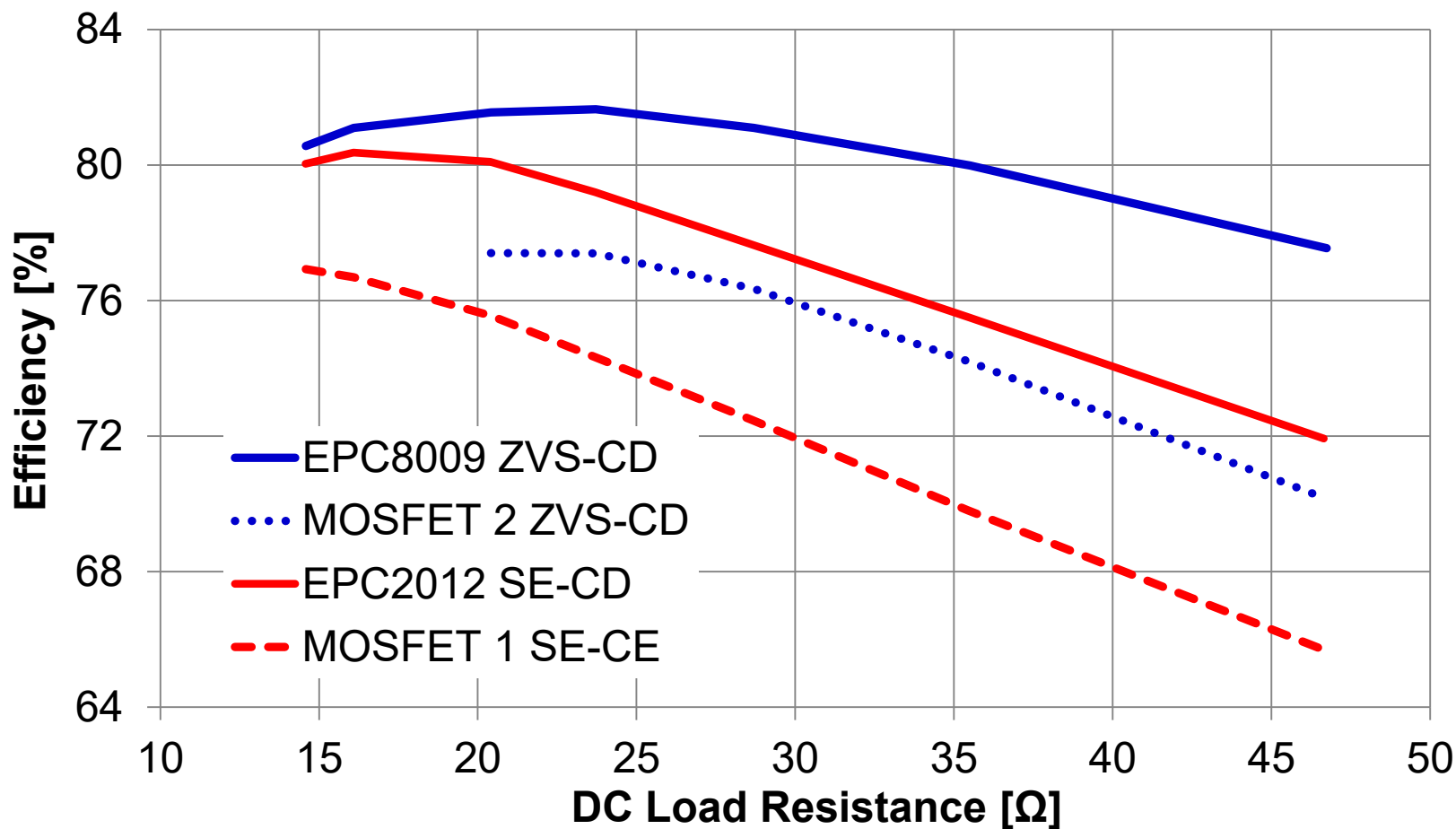


Load Variation Results

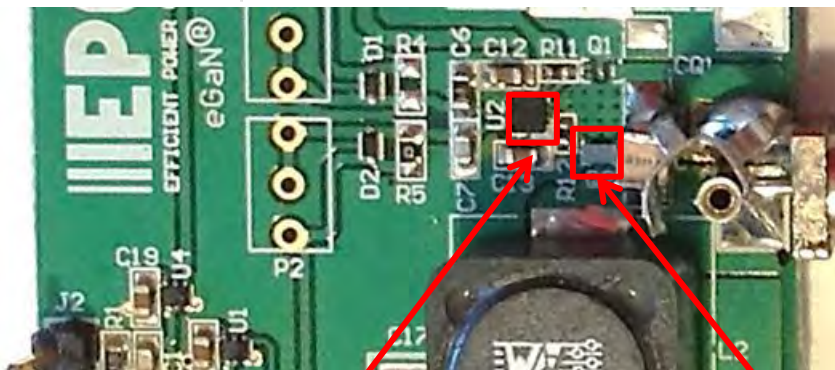


Load Regulation Comparison

Fixed DC load voltage, DC Load Resistance varied

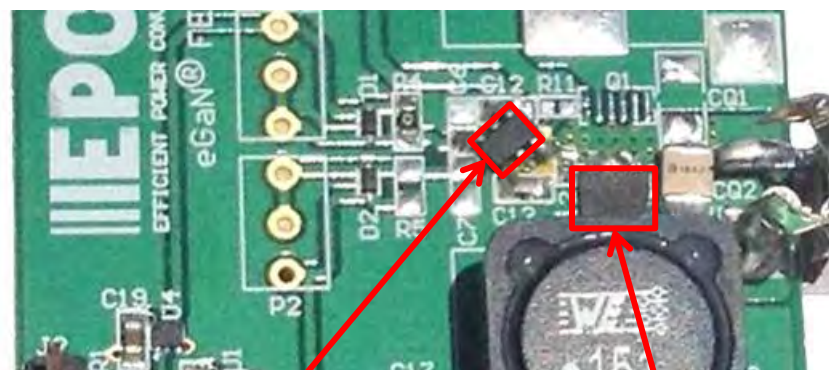


Class-E Thermal Performance



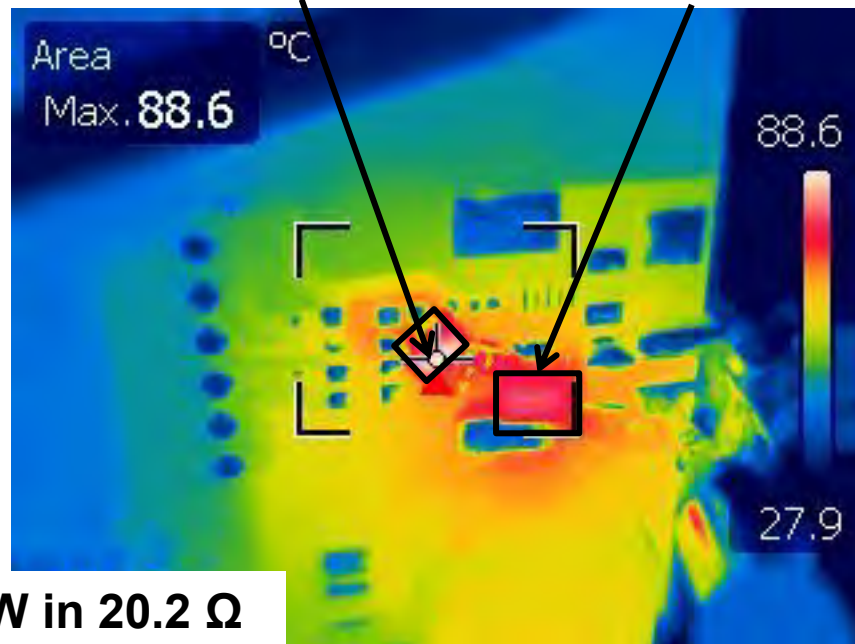
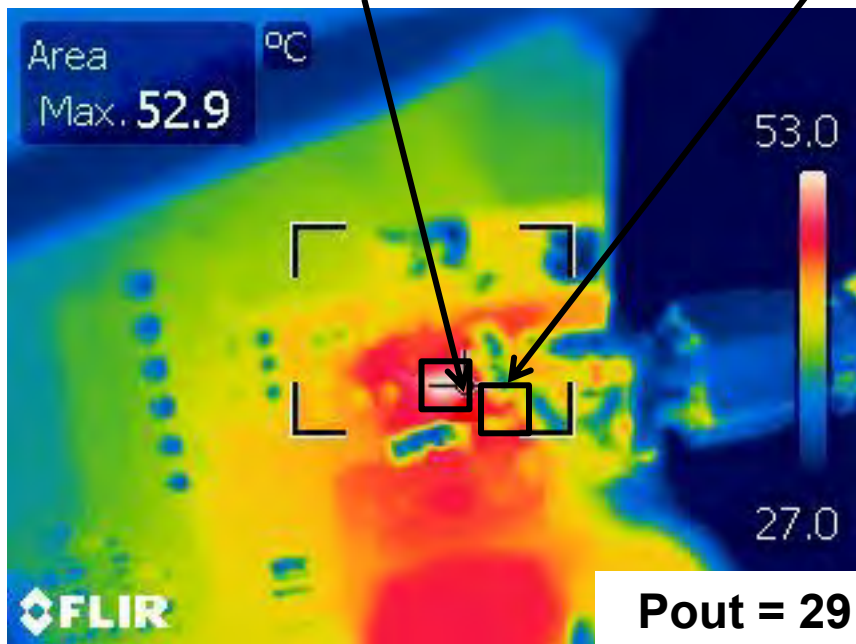
LM5113

eGaN FET



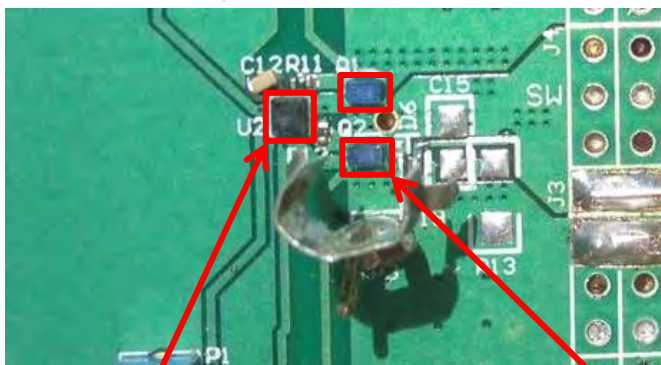
UCC27511

MOSFET



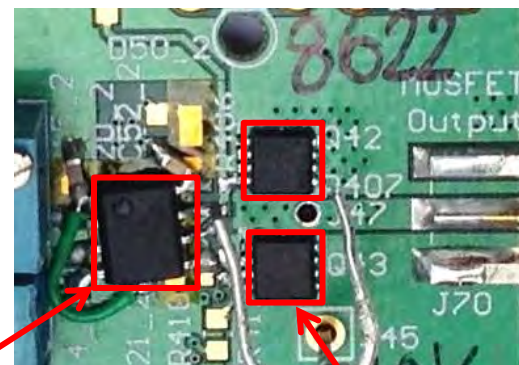
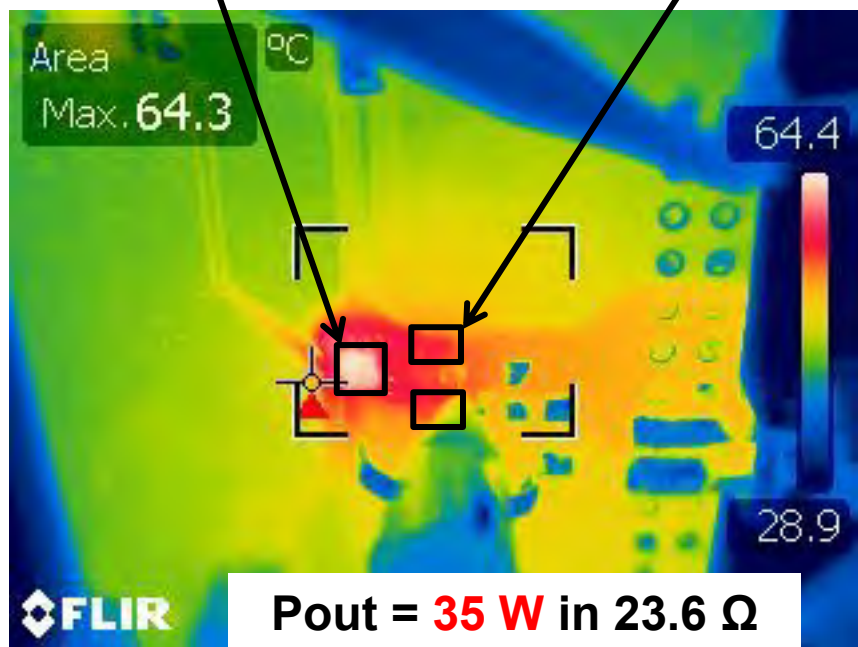
Pout = 29 W in 20.2 Ω

ZVS Class-D Thermal Performance



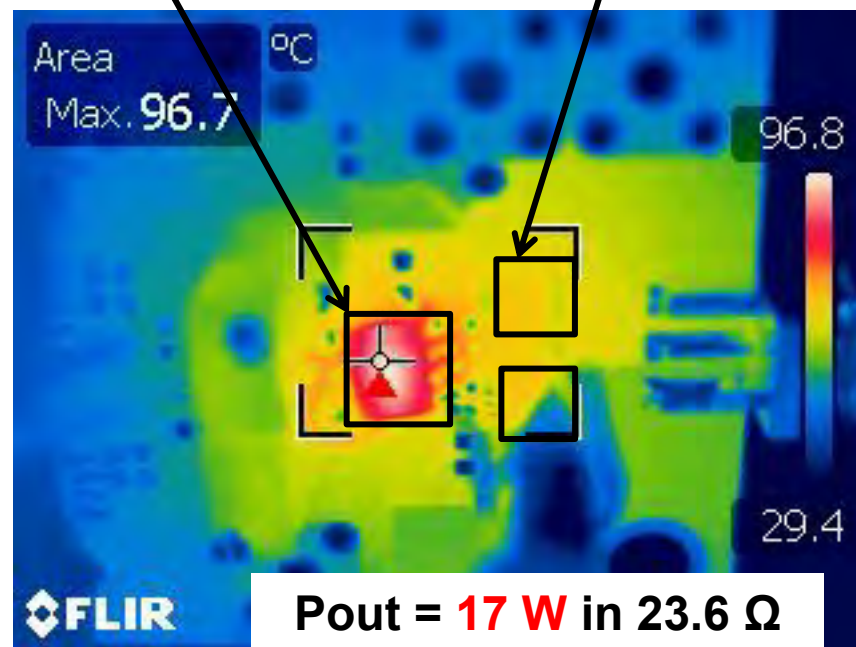
LM5113

EPC8009



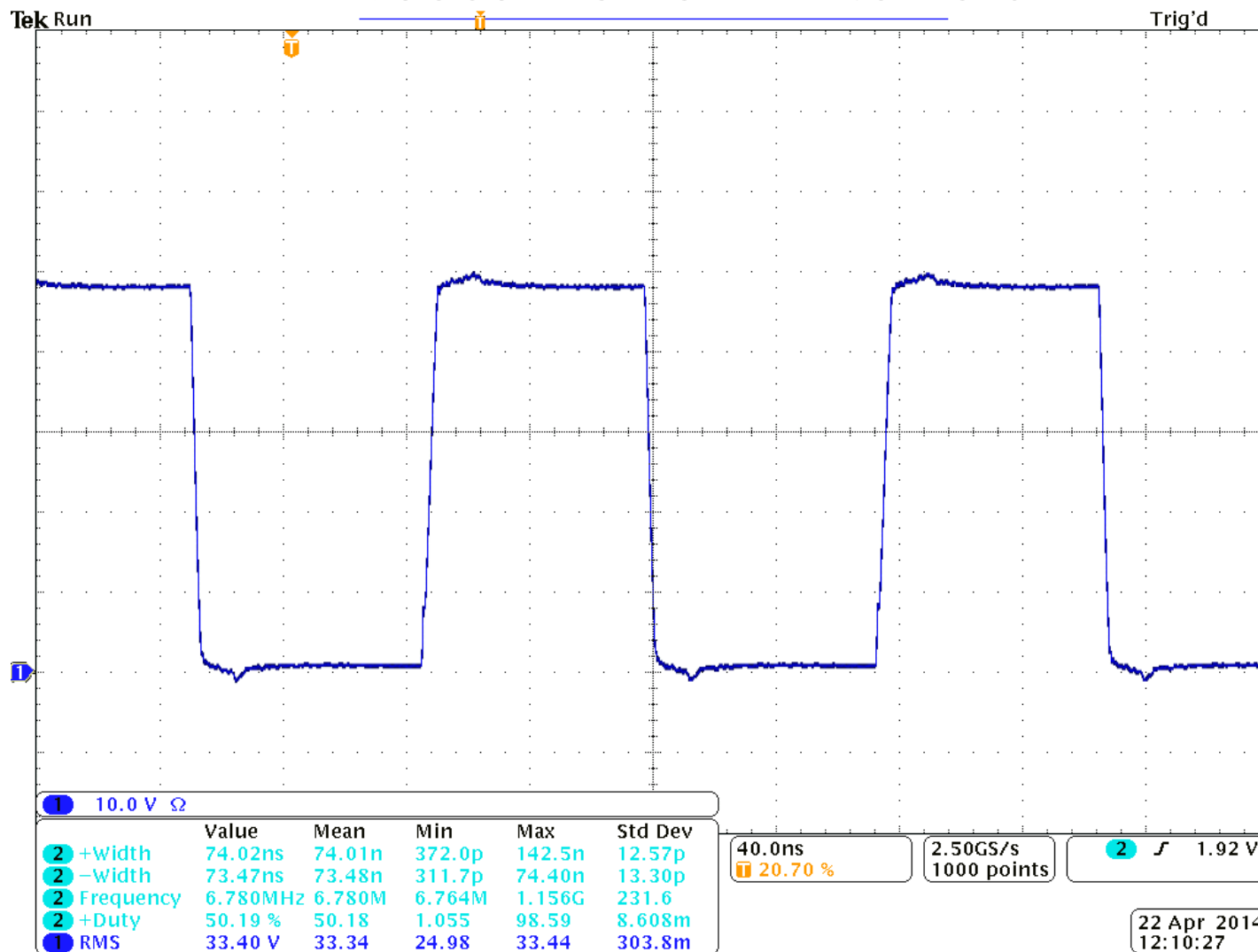
LM5107

MOSFET



ZVS Class-D waveforms

EPC8009 – 32.5 W into 23.6 Ω



Foreign Metal Object Response

- No simple means to detect a foreign object
- Ability to operate in presence of foreign metal object:
 - ZVS Class-D – very good
 - Class-E – very bad

Spacing	Voltage	Current	Temperature	Remarks	Severity
5 mm Class E	$6.4 \cdot V_{sup}$	$0.3 \cdot I_{nom}^*$	FET Very High*	Capacitive switching losses	High
5 mm ZVS Class D	$1 \cdot V_{sup}$	$0.02 \cdot I_{nom}$	40°C FET	Normal	Low

* Circuit cannot operate at full input voltage

- Class-E:
 - Good for Small load variations
 - Cannot tolerate foreign objects well
- ZVS Class-D:
- Highest Efficiency
- Good foreign object operation
- Lowest impact on tuned coil impedance (Low Z_{out})
- Lower Z_{OUT} reduces impact of load variation

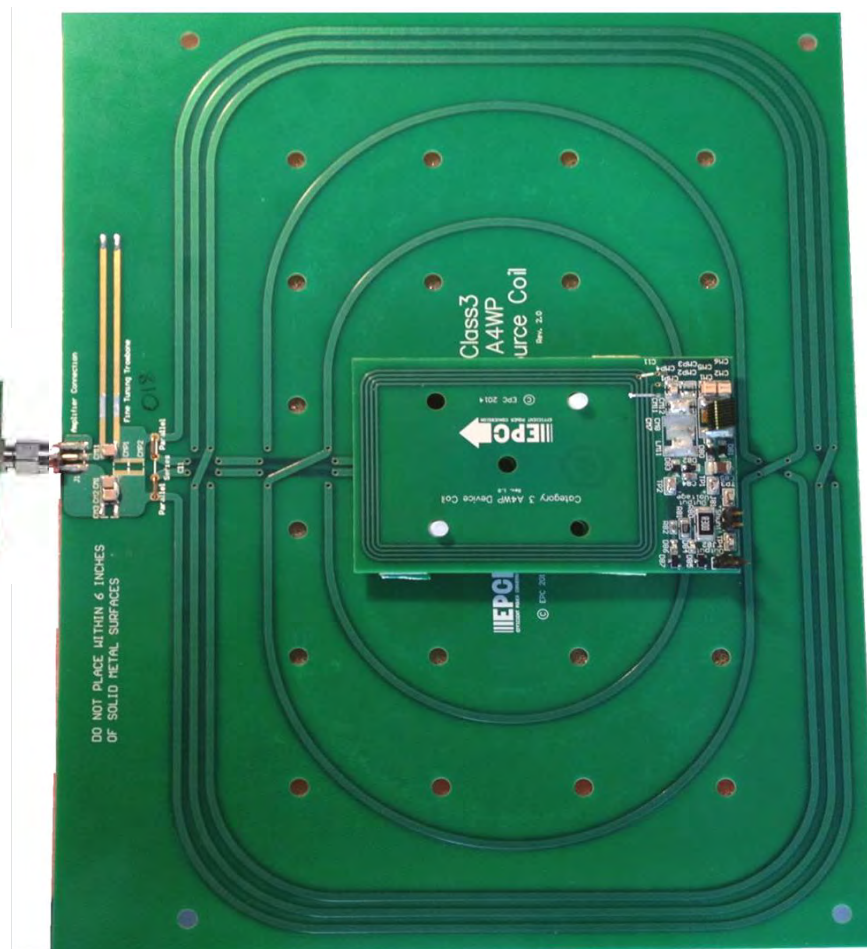
- New EPC Demo Boards
 - Single Ended and Differential ZVS Class-D
 - 6.78 MHz & 13.56 MHz
 - Includes Constant Current (CC) Pre-Regulator

- Single Ended and Differential ZVS Class-D
- Tuned A4WP class 3 and 1x Category 3 Coil set
- 6.78 MHz Design
- Constant Current Pre-Regulator



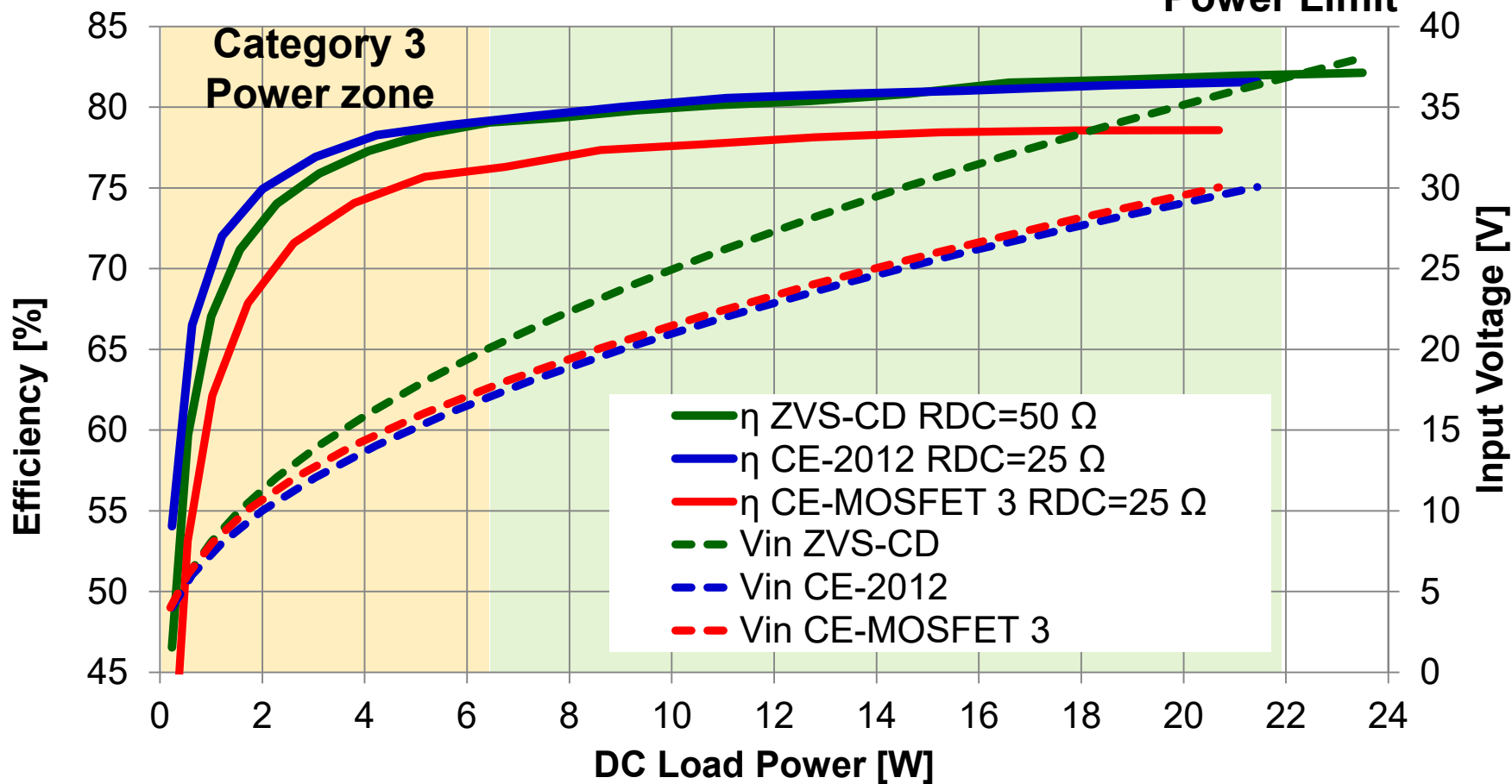
EPC9506/7

EPC9111 & EPC9112

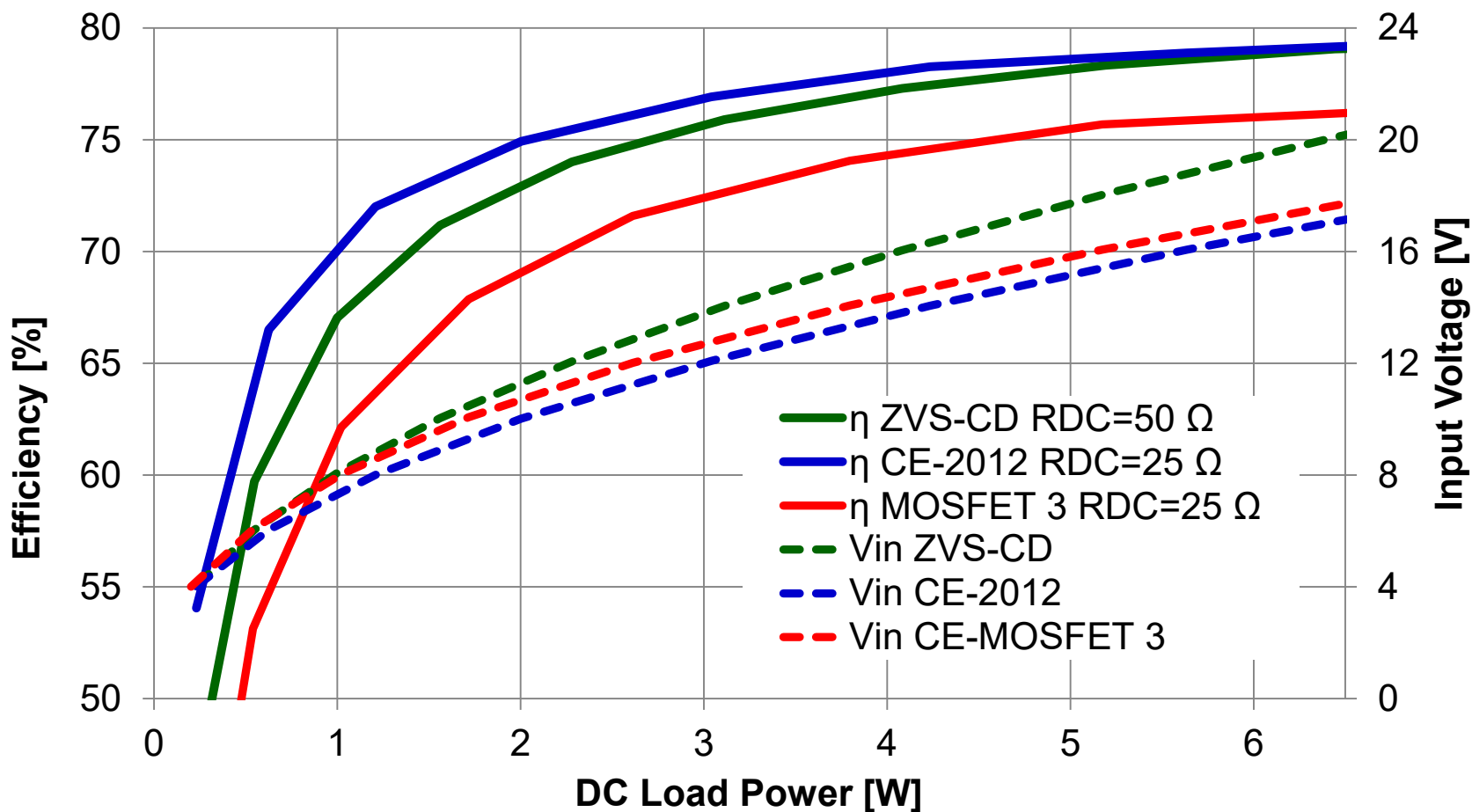


Peak Efficiency, Single load capability Variable Supply, Fixed Load

**Class 4
Power Limit**

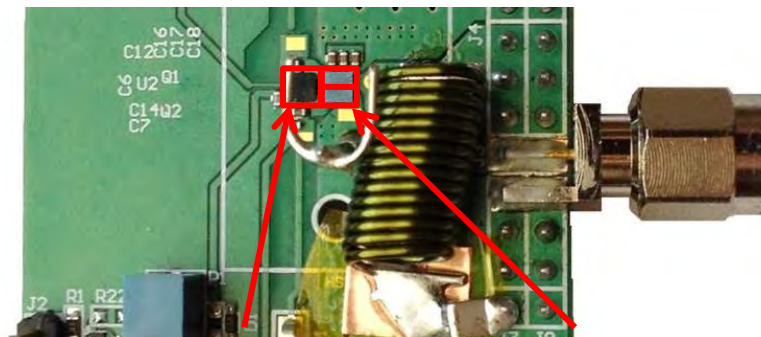


Peak Efficiency for Category 3 Load Variable Supply, Fixed Load



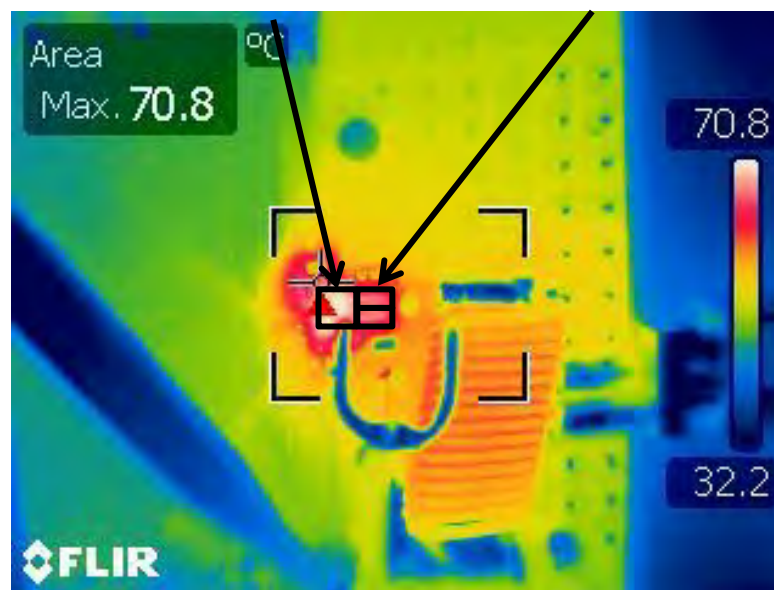
EPC8009 ZVS Class D Waveforms & Thermal

$$V_{DC} = 38 \text{ V}$$
$$P_{out} = 23.5 \text{ W}$$
$$R_{DCload} = 50.3 \text{ } \Omega$$



LM5113

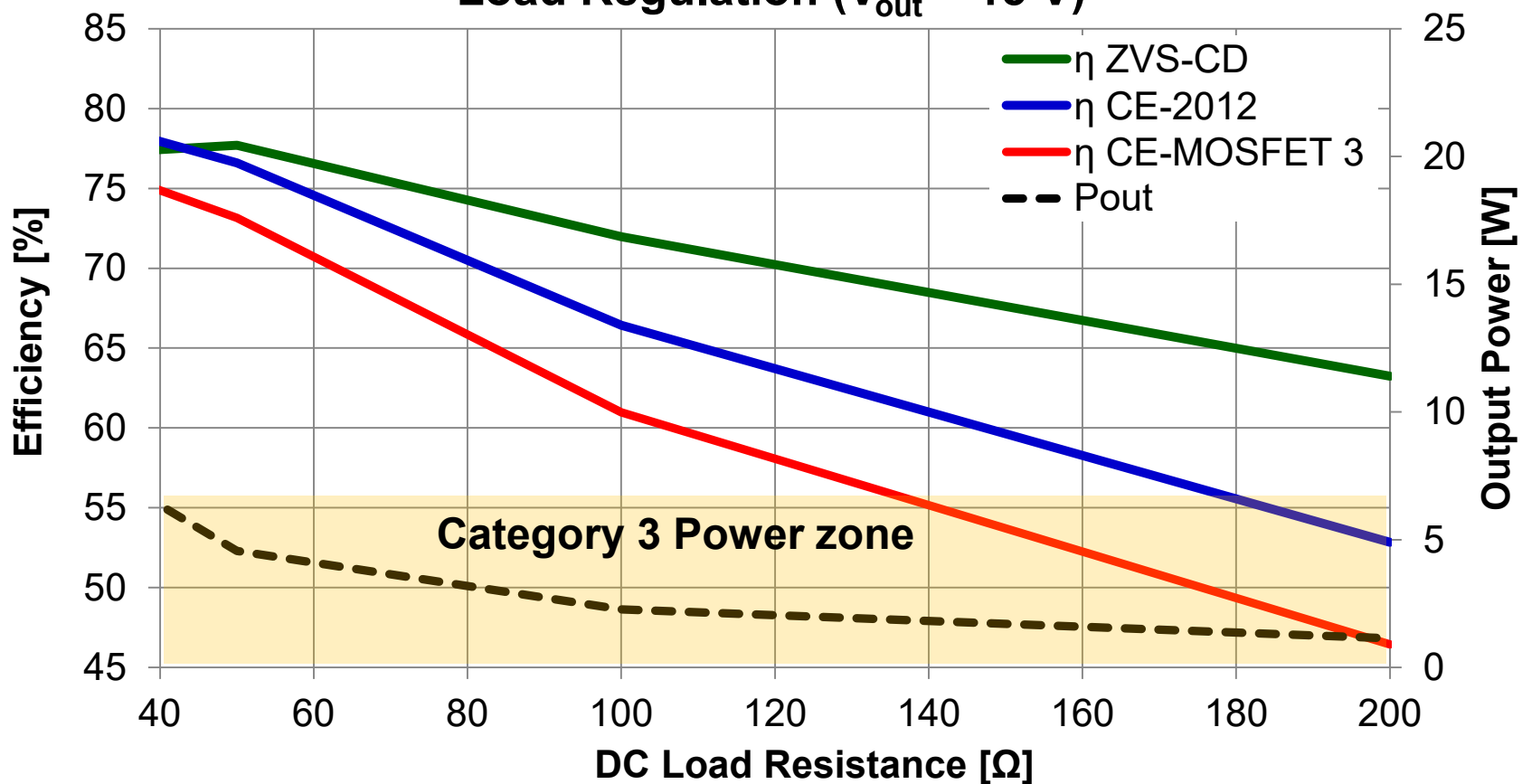
EPC8009



High Power, Peak Performance

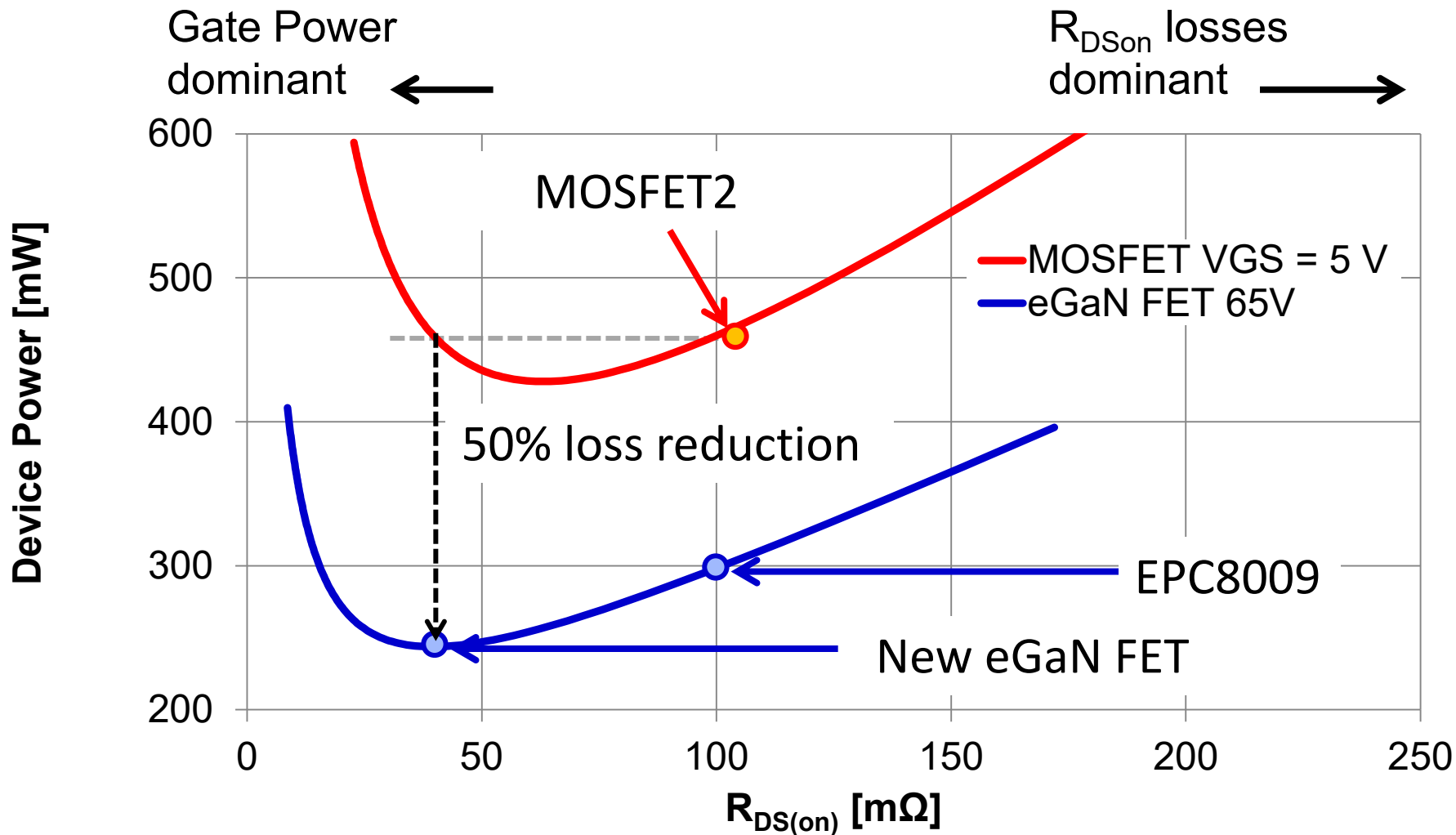
High Power Capability

Load Regulation ($V_{out} = 15\text{ V}$)



- Full A4WP impedance range testing
- Expand to Class 2 support
- eGaN FETs specifically designed for
Wireless Power
 - ZVS Class-D – Optimized Half bridge device
 - Differential Class-E – Dual FET common source

ZVS Class D topology



- eGaN[®] FETs are disruptive in Wireless Energy:
 - Enable Wireless Power
 - Yield Higher Efficiency than MOSFETs
 - Can operate at 6.78 MHz
 - Are low profile
 - Easy to use
 - Drive new topologies e.g. ZVS Class-D
 - Increasing IC vendor support for eGaN FETs
 - Increasing customer adoption



*The end of the road
for silicon.....*

*is the beginning of
the eGaN FET
journey!*